

Script generated by TTT

Title: Seidl: Programoptimierung (04.02.2013)

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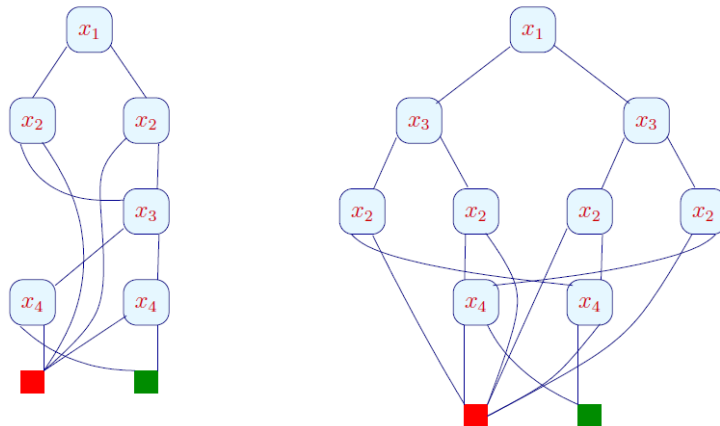
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Discussion:

- Originally, BDDs have been developed for circuit verification.
- Today, they are also applied to the verification of software ...
- A system state is encoded by a sequence of bits.
- A BDD then describes the set of all reachable system states.
- **Warning:** Repeated application of Boolean operations may increase the size dramatically !
- The variable ordering may have a dramatic impact ...

895

Example: $(x_1 \leftrightarrow x_2) \wedge (x_3 \leftrightarrow x_4)$



896

Discussion (2):

- In general, consider the function:

$$(x_1 \leftrightarrow x_2) \wedge \dots \wedge (x_{2n-1} \leftrightarrow x_{2n})$$

W.r.t. the variable ordering:

$$x_1 < x_2 < \dots < x_{2n}$$

the BDD has $3n$ internal nodes.

W.r.t. the variable ordering:

$$x_1 < x_3 < \dots < x_{2n-1} < x_2 < x_4 < \dots < x_{2n}$$

the BDD has more than 2^n internal nodes !!

- A similar result holds for the implementation of Addition through BDDs.

897

add

mem

Discussion (3):

- Not all Boolean functions have small BDDs :-)
 - Difficult functions:
 - multiplication;
 - indirect addressing ...
- ⇒ data-intensive programs cannot be analyzed in this way :-)

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Perspectives: Further Properties of Programs

Freeness: Is X_i possibly/always unbound ?

⇒

If X_i is always unbound, no indexing for X_i is required :-)

If X_i is never unbound, indexing for X_i is complete :-)

Pair Sharing: Are X_i, X_j possibly bound to terms t_i, t_j with

$$\text{Vars}(t_i) \cap \text{Vars}(t_j) \neq \emptyset \quad ?$$

⇒

Literals without sharing can be executed in parallel :-)

Remark:

Both analyses may profit from **Groundness** !

899

Example:

`bigger(X, Y)` ← $X = \text{elephant}, Y = \text{horse}$
`bigger(X, Y)` ← $X = \text{horse}, Y = \text{donkey}$
`bigger(X, Y)` ← $X = \text{donkey}, Y = \text{dog}$
`bigger(X, Y)` ← $X = \text{donkey}, Y = \text{monkey}$
`is_bigger(X, Y)` ← `bigger(X, Y)`
`is_bigger(X, Y)` ← `bigger(X, Z), is_bigger(Z, Y)`
 ← `is_bigger(elephant, dog)`

871

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871

A more realistic Example:

```
app(X, Y, Z) ← X = [], Y = Z
app(X, Y, Z) ← X = [H|X'], Z = [H|Z'], app(X', Y, Z')
               ← app(X, [Y, c], [a, b, Z])
```



X = a
Y = b
Z = c

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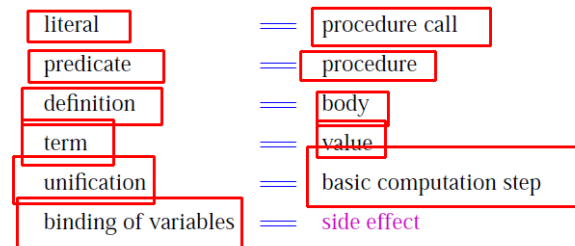
[] == the atom empty list
[H|Z] == binary constructor application
[a, b, Z] == Abbreviation for: [a|[b|[Z][]]]

Accordingly, a program p is constructed as follows:

```
t ::= a | X | _ | f(t1, ..., tn)
g ::= p(t1, ..., tk) | X = t
c ::= p(X1, ..., Xk) ← g1, ..., gr
q ::= ← g1, ..., gr
p ::= c1 ... cm q
```

- A **term** t either is an atom, a (possibly anonymous) variable or a constructor application.
- A **goal** g either is a literal, i.e., a predicate call, or a unification.
- A **clause** c consists of a **head** $p(X_1, \dots, X_k)$ together with **body** consisting of a sequence of goals.
- A **program** consists of a sequence of clauses together with a sequence of goals as **query**.

Procedural View of PuP-Programs:



Warning: Predicate calls ...

- do not return results!
- modify the caller solely through side effects :-)
- may fail. Then, the following definition is tried \implies **backtracking**

Inefficiencies:

- Backtracking:** • The matching alternative must be searched for
⇒ Indexing
- Since a successful call may still fail later, the stack can only be cleared if there are no pending alternatives.
- Unification:** • The translation possibly must switch between build and check several times.
- In case of unification with a variable, an **Occur Check** must be performed.
- Type Checking:** • Since Prolog is untyped, it must be checked at run-time whether or not a term is of the desired form.
- Otherwise, ugly errors could show up.

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$Z = a, X = [Y|Z]$

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Some Optimizations:

- Replacing last calls with jumps;
- Compile-time type inference;
- Identification of deterministic predicates ...

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877



Observation:

- In PuP, functions must be simulated through predicates.
- These then have designated input- and output parameters.
- Input parameters are those which are instantiated with a variable-free term whenever the predicate is called. These are also called **ground**.
- In the example, the first parameter of **app** is an input parameter.
- Unification with such a parameter can be implemented as **pattern matching** !
- Then we see that **app** in fact is deterministic !!!

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5.1 Groundness Analysis

A variable X is called **ground** w.r.t. a program execution π starting program entry and entering a program point v , if X is bound to a variable-free term.

Goal:

- Find all variables which are ground whenever a particular program point is reached !
- Find all arguments of a predicate which are ground whenever the predicate is called !

879

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Idea:

- Describe groundness by values from \mathbb{B} :
 - 1 == variable-free term;
 - 0 == term which contains variables.
- A set of variable assignments is described by Boolean functions :-)
 - $X \leftrightarrow Y$ == X is ground iff Y is ground.
 - $X \wedge Y$ == X and Y are ground.

880

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Idea (cont.):

- The constant function 0 denotes an unreachable program point.
- Occurring sets of variable assignments are closed under substitution. This means that for every occurring function $\phi \neq 0$,
$$\phi(1, \dots, 1) = 1$$
These functions are called **positive**.
 - The set of all positive functions is called **Pos**.
Ordering: $\phi_1 \sqsubseteq \phi_2$ if $\phi_1 \Rightarrow \phi_2$.
 - In particular, the least element is 0 :-)

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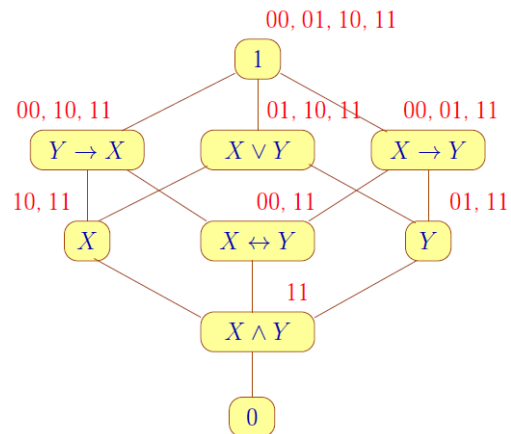
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Example:



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Remarks:

- Not all positive functions are monotonic !!!
- For k variables, there are $2^{2^k-1} + 1$ many functions.
- The height of the complete lattice is 2^k .
- We construct an interprocedural analysis which for every predicate p determines a (monotonic) transformation

$$[[p]]^\sharp : \text{Pos} \rightarrow \text{Pos}$$

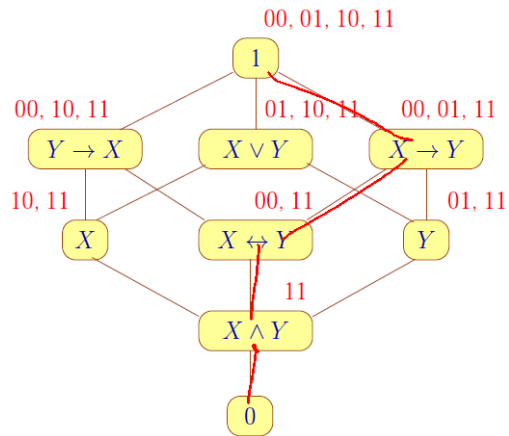
- For every clause, $p(X_1, \dots, X_k) \Leftarrow g_1, \dots, g_n$ we obtain the constraint:

$$[[p]]^\sharp \psi \sqsubseteq \exists X_{k+1}, \dots, X_m. [[g_n]]^\sharp (\dots ([[g_1]]^\sharp \psi) \dots)$$

// m number of clause variables

883

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Abstract Unification:

$$[[X = t]]^\sharp \psi = \psi \wedge (X \leftrightarrow X_1 \wedge \dots \wedge X_r)$$

if $\text{Vars}(t) = \{X_1, \dots, X_r\}$.

Abstract Literal:

$$[[q(s_1, \dots, s_k)]]^\sharp \psi = \text{combine}_{s_1, \dots, s_k}^\sharp(\psi, [[q]]^\sharp(\text{combine}_{s_1, \dots, s_k} \psi))$$

// analogous to procedure call !!

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Abstract Unification:

$$\begin{aligned} \llbracket X = t \rrbracket^\# \psi &= \psi \wedge (X \leftrightarrow X_1 \wedge \dots \wedge X_r) \\ \text{if } \text{Vars}(t) &= \{X_1, \dots, X_r\}. \end{aligned}$$

Abstract Literal:

$$\llbracket q(s_1, \dots, s_k) \rrbracket^\# \psi = \text{combine}_{s_1, \dots, s_k}^\#(\psi, \llbracket q \rrbracket^\#(\text{enter}_{s_1, \dots, s_k}^\# \psi))$$

// analogous to procedure call !!

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Thereby:

$$\begin{aligned} \text{enter}_{s_1, \dots, s_k}^\# \psi &= \text{ren}(\exists X_1, \dots, X_m. \llbracket \bar{X}_1 = s_1, \dots, \bar{X}_k = s_k \rrbracket^\# \psi) \\ \text{combine}_{s_1, \dots, s_k}^\#(\psi, \psi_1) &= \exists \bar{X}_1, \dots, \bar{X}_r. \psi \wedge \llbracket \bar{X}_1 = s_1, \dots, \bar{X}_k = s_k \rrbracket^\#(\overline{\text{ren}} \psi_1) \end{aligned}$$

where

$$\begin{aligned} \exists X. \phi &= \phi[0/X] \vee \phi[1/X] \\ \text{ren } \phi &= \phi[X_1/\bar{X}_1, \dots, X_k/\bar{X}_k] \\ \overline{\text{ren}} \phi &= \phi[\bar{X}_1/X_1, \dots, \bar{X}_r/X_r] \end{aligned}$$

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