### Script generated by TTT

Title: Petter: Programmiersprachen (11.11.2015)

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### **Deadlocks with Monitors**

#### **Definition (Deadlock)**

A deadlock is a situation in which two processes are waiting for the respective other to finish, and thus neither ever does.

(The definition generalizes to a set of actions with a cyclic dependency.)

Consider this Java class:

Sequence leading to a deadlock:

- class Foo { public Foo other = null; public synchronized void bar() ... if (\*) other bar(); ...
- and two instances:

```
Foo a = new Foo();
Foo b = new Foo();
a.other = b; b.other = a
// in parallel:
a.bar() || b.bar();
```

- threads A and B execute a.bar() and b.bar()
- a.bar() acquires the monitor of a
- b.bar() acquires the monitor of b
- A happens to execute other.bar()
- A blocks on the monitor of b
- B happens to execute other.bar()

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How can this situation be avoided?

Atomic Executions, Locks and Monitors

### **Treatment of Deadlocks**

Deadlocks occur if the following four conditions hold [Coffman et al.(1971)Coffman, Elphick, and Shoshani]:

- mutual exclusion: processes require exclusive access
- wait for: a process holds resources while waiting for more
- on preemption: resources cannot be taken away form processes
- (a) circular wait: waiting processes form a cycle

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The occurrence of deadlocks can be:

- *ignored*: for the lack of better approaches, can be reasonable if deadlocks are rare
- detection: check within OS for a cycle, requires ability to preempt
- prevention: design programs to be deadlock-free
- avoidance: use additional information about a program that allows the OS to schedule threads so that they do not deadlock

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→ prevention is the only safe approach on standard operating systems.

- can be achieve using *lock-free* algorithms
- but what about algorithms that require locking?

### **Deadlock Prevention through Partial Order**



Observation: A cycle cannot occur if locks can be partially ordered.

### Definition (lock sets)

Let L denote the set of locks. We call  $\lambda(p) \subseteq L$  the lock set at p, that is, the set of locks that may be in the "acquired" state at program point p.

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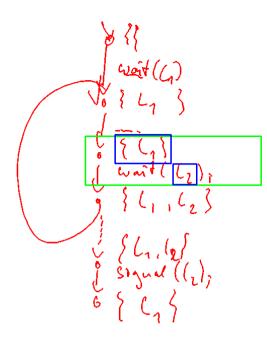
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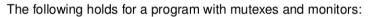
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Suppose a program blocks on semaphores (mutexes)  $L_S$  and on monitors  $L_M$  such that  $L=L_S\cup L_M$ .

#### Theorem (freedom of deadlock for monitors)

If  $\forall a \in L_S$  and  $\forall a \in L_M$ ,  $b \in L$  and  $b \prec a \Rightarrow a = b$  then the program is free of deadlocks.

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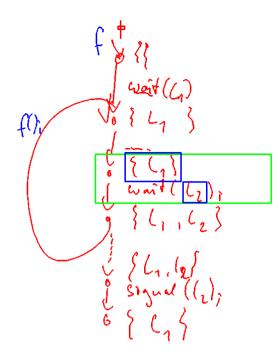
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Note: the set L contains *instances* of a lock.

- the set of lock instances can vary at runtime
- if we statically want to ensure that deadlocks cannot occur:
  - summarize every lock/monitor that may have several instances into one
  - $lackbox{ a summary lock/monitor } ar{a} \in L_M \ ext{represents several concrete ones}$
  - ▶ thus, if  $\bar{a} \prec \bar{a}$  then this might not be a self-cycle
  - $\leadsto$   $\,$  require that  $\bar{a}\not\prec\bar{a}$  for all summarized monitors  $\bar{a}\in L_M$



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## **Avoiding Deadlocks in Practice**

How can we verify that a program contains no deadlocks?

- ullet identify mutex locks  $L_S$  and summarized monitor locks  $L_M^s\subseteq L_M$
- identify non-summary monitor locks  $L_M^n = L_M \setminus L_M^s$
- sort locks into ascending order according to lock sets
- check that no cycles exist except for self-cycles of non-summary monitors

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What to do when lock order contains cycle?

- determining which locks may be acquired at each program point is undecidable → lock sets are an approximation
- ullet an array of locks in  $L_S$ : lock in increasing array index sequence
- if  $l \in \lambda(P)$  exists  $l' \prec l$  is to be acquired  $\leadsto$  change program: release l, acquire l', then acquire l again  $\leadsto$  inefficient
- $\bullet$  if a lock set contains a summarized lock  $\bar{a}$  and  $\bar{a}$  is to be acquired, we're stuck

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an example for the latter is the Foo class: two instances of the same class call each other

Atomic Executions, Locks and Monitors

**Example: Deadlock freedom** 

Is the example deadlock free? Consider its skeleton:

```
double-ended queue: removal
  void PopRight() {
   wait(q->t);
   if (*) { signal(q->t); return; }
   if (c) wait(q->s)
   if (c) signal(q->s);
   signal(q->t);
```

### **Refining the Queue: Concurrent Access**



Add a second lock s->t to allow concurrent removal/peeking:

```
double-ended queue: removal
 int PopRight(DQueue* q) {
   QNode* oldRightNode;
   wait(q->t); // wait to enter the critical section
   QNode* rightSentinel = q->right;
   oldRightNode = rightSentinel->left;
   if (oldRightNode==leftSentinel) { signal(q->t); return -1; }
   QNode* newRightNode = oldRightNode->left;
   int c = newRightNode==leftSentinel;
   if (c) wait(q->s):
   newRightNode->right = rightSentinel;
   rightSentinel->left = newRightNode;
   if (c) signal(q->s);
   signal(q->t); // signal that we're done
   int val = oldRightNode->val;
   free(oldRightNode);
   return val;
```

### **Example: Deadlock freedom**



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- ir PushLeft the lock set for s is empty
- here, the lock set of s is {t}
- $t \triangleleft s$  and transitive closure is  $t \prec s$
- when the program cannot deadlock

### **Atomic Execution and Locks**



Consider replacing the specific locks with atomic annotations:

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Locks Roundup

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```

- nested atomic blocks still describe one atomic execution
- → locks convey additional information over atomic
- locks cannot easily be recovered from atomic declarations

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### **Outlook**



Writing atomic annotations around sequences of statements is a convenient way of programming.

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Writing atomic annotations around sequences of statements is a convenient way of programming.

Idea of mutexes: Implement atomic sections with locks:

- a single lock could be used to protect all atomic blocks
- more concurrency is possible by using several locks
  - ► see the PushLeft, PopRight example
- some statements might modify variables that are never read by other threads → no lock required
- statements in one atomic block might access variables in a different order to another atomic block → deadlock possible with locks implementation
- ullet creating too many locks can decrease the performance, especially when required to release locks in  $\lambda(l)$  when acquiring l

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### **Concurrency across Languages**

In most systems programming languages (C,C++) we have

we can implement wait-free algorithms

• the ability to use atomic operations

### **Concurrency across Languages**

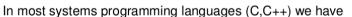
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In Java, C# and other higher-level languages

- provide monitors and possibly other concepts
- often simplify the programming but incur the same problems

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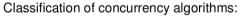
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language	barriers	wait-/lock-free	semaphore	mutex	monitor
C,C++	<b>√</b>	<b>√</b>	<b>√</b>	<b>√</b>	(a)
Java,C#	-	(b)	(c)	<b>√</b>	<b>√</b>

- (a) some pthread implementations allow a *reentrant* attribute
- (b) newer API extensions ( java.util.concurrent.atomic.\* and System. Threading. Interlocked resp.)
- (c) simulate semaphores using an object with two synchronized methods

### **Summary**



- wait-free, lock-free, locked
- next on the agenda: transactional

#### Wait-free algorithms:

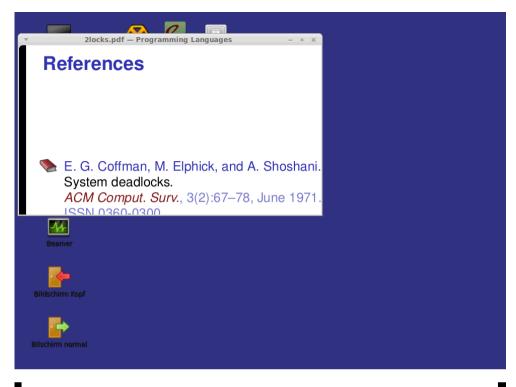
- never block, always succeed, never deadlock, no starvation
- very limited in what they can do

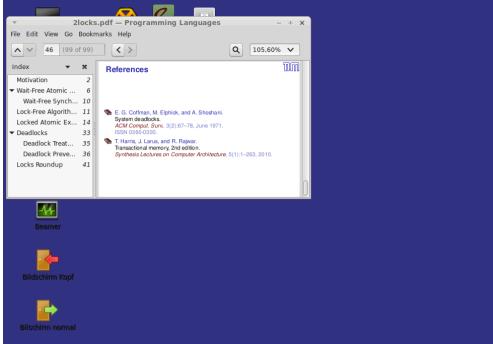
### Lock-free algorithms:

- never block, may fail never deadlock, may starve
- invariant may only span a few bytes (8 on Intel)

### Locking algorithms:

- can guard arbitrary code
- can use several locks to enable more fine grained concurrency
- may deadlock
- semaphores are not re-entrant, monitors are
- → use algorithm that is best fit







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### **Programming Languages**

Concurrency: Transactions

Dr. Michael Petter Winter term 2015

## **Abstraction and Concurrency**

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Two fundamental concepts to build larger software are:

abstraction: an object storing certain data and providing certain

functionality may be used without reference to its internals

composition: several objects can be combined to a new object without

interference

Concurrency: Transactions Motivation

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Both, *abstraction* and *composition* are closely related, since the ability to compose depends on the ability to abstract from details.

Consider an example:

- a linked list data structure exposes a fixed set of operations to modify the list structure, such as PushLeft and ForAll
- a set object may internally use the list object and expose a set of operations, including PushLeft

The Insert operations uses the ForAll operation to check if the element already exists and uses PushLeft if not.

Concurrency: Transactions

Motivation

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composition: several objects can be combined to a new object without

interference

Both, *abstraction* and *composition* are closely related, since the ability to compose depends on the ability to abstract from details.

Consider an example:

- a linked list data structure exposes a fixed set of operations to modify the list structure, such as PushLeft and ForAll
- a set object may internally use the list object and expose a set of operations, including PushLeft

The Insert operations uses the ForAll operation to check if the element already exists and uses PushLeft if not.

Wrapping the linked list in a mutex does not help to make the *set* thread-safe.

- → wrap the two calls in Insert in a mutex
- but other list operations can still be called → use the same mutex

Concurrency: Transaction

Motivatio

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### **Abstraction and Concurrency**

Two fundamental concepts to build larger software are:

abstraction: an object storing certain data and providing certain

functionality may be used without reference to its internals composition: several objects can be combined to a new object without

interference

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Wrapping the linked list in a mutex does not help to make the set thread-safe.

- $\leadsto$  wrap the two calls in  ${\tt Insert}$  in a mutex
- but other list operations can still be called → use the same mutex
- while sequential algorithms, thread-safe algorithms cannot always be composed to give new thread-safe algorithms

### **Transactional Memory [2]**



Idea: automatically convert atomic blocks into code that ensures atomic execution of the statements.

```
atomic {
  // code
  if (cond) retry;
atomic {
    // more code
  }
  // code
}
```

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### **Transactional Memory [2]**



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```

#### Execute code as *transaction*:

- execute the code of an atomic block
- nested atomic blocks act like a single atomic block
- check that it runs without conflicts due to accesses from another thread
- if another thread interferes through conflicting updates:
  - undo the computation done so far
  - re-start the transaction
- provide a retry keyword similar to the wait of monitors

Concurrency: Transactions

Motivation

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### **Managing Conflicts**



#### **Definition (Conflicts)**

A conflict *occurs* when accessing the same piece of data, a conflict is *detected* when the TM system observes this, it is *resolved* when the TM system takes action (by delaying or aborting a transaction).

Design choices for transactional memory implementations:

- optimistic vs. pessimistic concurrency control:
  - pessimistic: detection/resolution when the conflict is about to occur
    - \* resolution here is usually *delaying* one transaction
    - \* can be implemented using *locks*: deadlock problem
  - optimistic: detection and resolution happen after a conflict occurs
    - \* resolution here must be aborting one transaction
    - ★ need to repeat aborted transaction: livelock problem
- eager vs. lazy version management: how read and written data are managed during the transaction
  - eager: writes modify the memory and an undo-log is necessary if the transaction aborts
  - lazy: writes are stored in a redo-log and modifications are done on committing

Concurrency: Transactions

Transaction Semantics

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Transaction Semant

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